

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	"4649498".pn. and (status adj data) and (integrated same circuit) and characteriz \$5 and (state same variable)	USPAT	OR	OFF	2009/01/23 09:48
L2	1	"4649498".pn.	USPAT	OR	OFF	2009/01/23 09:49
L3	1	L2 and (status adj data)	USPAT	OR	OFF	2009/01/23 09:49
L4	0	L2 and (status adj data) and (integrate same circuit) and chara\$7	USPAT	OR	OFF	2009/01/23 09:49
L5	0	L2 and (status adj data) and (integrate same circuit) and characterization	USPAT	OR	OFF	2009/01/23 09:49
L6	0	L2 and (status adj data) and (integrate same circuit) and characterization	USPAT	OR	OFF	2009/01/23 09:49
L7	0	L2 and (status adj data) and (integrate same circuit) and chara\$9	USPAT	OR	OFF	2009/01/23 09:50
L8	0	L2 and (status adj data) and (integrate same circuit) and (state same variable)	USPAT	OR	OFF	2009/01/23 09:50
L9	1	"4588950".pn. and (status adj data)	USPAT	OR	OFF	2009/01/23 09:52
L10	1	"4588950".pn. and (status adj data) and (integrated same circuit) and characterization	USPAT	OR	OFF	2009/01/23 09:52
L11	0	"4588950".pn. and (status adj data) and (integrated same circuit) and characterization and (state adj variable)	USPAT	OR	OFF	2009/01/23 09:52

L12	0	"4588950".pn. and (status adj data) and (integrated same circuit) and characterization and (state same variable)	USPAT	OR	OFF	2009/01/23 09:52
L13	1	"4588950".pn. and (status adj data) and (integrated same circuit) and characterization and state	USPAT	OR	OFF	2009/01/23 09:56
L14	0	"4588950".pn. and (status adj data) and (integrated same circuit) and characterization and variable	USPAT	OR	OFF	2009/01/23 09:57
L15	1	"4588950".pn. and (status adj data)	USPAT	OR	OFF	2009/01/23 09:57
L16	1088	(state same machine) and (state adj variable)	USPAT	OR	OFF	2009/01/23 09:59
L17	49	(state same machine same control) and (state adj variable) and (circuit adj diagram)	USPAT	OR	OFF	2009/01/23 10:00
L18	79	(state same machine same control) and (state adj variable) and (circuit adj diagram)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/01/23 10:00
L19	22	(state same machine same control) and (state adj variable) and (circuit adj diagram) and (error same (state or indication or status))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/01/23 10:01
L20	2	(machine adj control) and (state adj variable) and (circuit adj diagram) and (error same (state or indication or status))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/01/23 10:03
L21	28	(machine same control) and (state adj variable) and (circuit adj diagram) and (error same (state or indication or status))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/01/23 10:03

L22	64	(machine same control) and (state adj variable) and (circuit adj diagram) and (error same (state or indication or status))	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:06
L23	3	(machine same control) and (state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and (status adj data)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:06
L24	37	(state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and (status adj data)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:07
L25	313	(state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and (control controller)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:11
L26	69	(state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and ((control controller) same machine)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:11
L27	1	(physical adj state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and ((control controller) same machine)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:12
L28	69	(state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and ((control controller) same machine)	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:12
L29	22	(state adj variable) and (circuit adj diagram) and (error same (state or indication or status)) and ((control controller) same machine) and display	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:12

L30	4	(state adj variable) and (test same (circuit adj diagram)) and (error same (state or indication or status)) and ((control controller same machine) and display	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:29
L31	27	(state adj variable) and (test same (semiconductor)) and (error same (state or indication or status)) and ((control controller same machine) and display	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:31
L32	31	(state adj variable) and (test same (semiconductor)) and (error same (state or indication or status)) and display	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:33
L33	44	(state adj variable) and (test same (semiconductor)) and (error) and display	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/23 10:34

1/23/09 10:52:21 AM

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